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最后，介绍了使用工具软件和Altera公司的FIR MagaCore在FPGA中对线性相位FIR滤波器的设计实现和仿真结果，并通过仿真结果考察了滤波器系数位数对滤波器性能造成的影响 ...

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して対称型FIRフィルタ (50タップ、8ビット・ データ、12ビット係数のソリューション) を実現したときのものです ... MagaCoreファンクションを新たに追加しました。 ...

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are implemented using **finite impulse response** (FIR) filters. FIR filters are computationally more expensive to implement than infinite **impulse response** ...

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The IIR-compiler is interactive and displays dynamically the filter > > response in the ... Philippe Molson > > Altera Corporation > > > > "gallenm" ...

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... already fully parameterizable : For the Decimating Filter see the FIR Compiler (1.1): ... A DDC is basically of a NCO, a decimator and of an FIR filter. ...

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... in the >>> IIR-Compiler such as ... efficient IIR filters for >>> Cyclone ...

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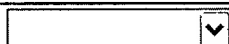
... IIR-Compiler such as multi-bit serial. > > I hope that this will help you to build
efficient IIR filters for > Cyclone device. > > **Philippe Molson** > Altera ...

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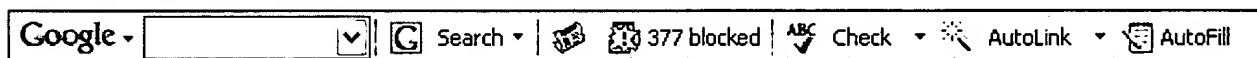
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the design against completing. the project quickly. [Communication Systems Design].

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